

CLAIMS

What is claimed is:

1 1. A method for automatically routing an integrated circuit, the method comprising
2 the computer-implemented steps of:
3 receiving integrated circuit layout data that defines a set of two or more integrated
4 circuit devices to be included in the integrated circuit;
5 receiving integrated circuit connection data that specifies one or more electrical
6 connections to be made between the integrated circuit devices;
7 determining, based upon the integrated circuit layout data and the integrated
8 circuit connection data, a set of one or more routing indicators that
9 indicate a set of one or more preferable intermediate routing locations for
10 a routing path between first and second integrated circuit devices from the
11 set of two or more integrated circuit devices;
12 determining, based upon the integrated circuit layout data, the integrated circuit
13 connection data and the set of one or more routing indicators, the routing
14 path between the first and second integrated circuit devices, wherein the
15 routing path satisfies specified design criteria; and
16 updating the integrated circuit layout data to generate updated integrated circuit
17 layout data that reflects the routing path between the first and second
18 integrated circuit devices.

1 2. The method as recited in Claim 1, wherein determining the routing path includes
2 determining, based upon the integrated circuit layout data, the integrated circuit
3 connection data, bias direction criteria and straying limit criteria, the routing path
4 between the first and second integrated circuit devices, wherein the bias direction
5 criteria specifies a preferred routing direction for a routing path between first and

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6 second integrated circuit devices from the set of two or more integrated circuit
7 devices and the straying limit criteria defines a routing region in which the routing
8 path between the first and second integrated circuit devices may be placed.

1 3. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes
3 identifying one or more obstacles that block the routing path,
4 determining, based upon the integrated circuit layout data, the integrated circuit
5 connection data and the one or more obstacles, one or more additional
6 routing indicators, and
7 determining, based upon the integrated circuit layout data, the integrated circuit
8 connection data, the set of one or more routing indicators and the one or
9 more additional routing indicators, the routing path between the first and
10 second integrated circuit devices.

1 4. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes
3 identifying one or more obstacles that block the routing path,
4 changing specified straying limit criteria that defines a routing region in which the
5 routing path between the first and second integrated circuit devices may be
6 placed to generate changed specified straying limit criteria that defines a
7 modified routing region, and
8 determining, based upon the integrated circuit layout data, the integrated circuit
9 connection data, the set of one or more routing indicators and the changed
10 specified straying limit criteria, the routing path between the first and
11 second integrated circuit devices.

1 5. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes
3 identifying one or more obstacles that block the routing path,
4 determining a set of one or more layer changes to allow the routing path to avoid
5 the one more obstacles, and
6 determining, based upon the integrated circuit layout data, the integrated circuit
7 connection data, the set of one or more routing indicators and the set of
8 one or more layer changes, the routing path between the first and second
9 integrated circuit devices.

1 6. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes
3 identifying one or more obstacles that block the routing path,
4 determining a set of one or more bends to be included in the routing path to avoid
5 the one more obstacles, and
6 determining, based upon the integrated circuit layout data, the integrated circuit
7 connection data, the set of one or more routing indicators and the set of
8 one or more bends, the routing path between the first and second
9 integrated circuit devices.

1 7. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes
3 identifying one or more obstacles that block the routing path,
4 determining one or more portions of the routing path to be ripped up and rerouted,
5 and
6 determining, based upon the integrated circuit layout data, the integrated circuit
7 connection data, the set of one or more routing indicators and the one or

8 more portions of the routing path to be ripped up and rerouted, the routing
9 path between the first and second integrated circuit devices.

1 8. The method as recited in Claim 7, wherein determining the routing path between
2 the first and second integrated circuit devices further includes
3 determining one or more portions of one or more other routing paths to be ripped
4 up and rerouted, and
5 determining, based upon the integrated circuit layout data, the integrated circuit
6 connection data, the set of one or more routing indicators, the one or more
7 portions of the routing path to be ripped up and rerouted and the one or
8 more portions of the one or more other routing paths to be ripped up and
9 rerouted, the routing path between the first and second integrated circuit
10 devices.

1 9. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices further includes
3 identifying one or more obstacles that block the routing path,
4 determining one or more portions of one or more other routing paths to be ripped
5 up and rerouted, and
6 determining, based upon the integrated circuit layout data, the integrated circuit
7 connection data, the set of one or more routing indicators and the one or
8 more portions of the one or more other routing paths to be ripped up and
9 rerouted, the routing path between the first and second integrated circuit
10 devices.

1 10. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes

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3 identifying one or more obstacles that block the routing path, and
4 determining, based upon the integrated circuit layout data, the integrated circuit
5 connection data and the set of one or more routing indicators and the
6 routing path between the first and second integrated circuit devices,
7 wherein the routing path is routed from the second integrated circuit
8 device to the first integrated circuit device.

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1 11. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes
3 identifying one or more obstacles that block the routing path,
4 determining one or more locations to employ corner clipping to provide additional
5 space for routing the routing path, and
6 determining, based upon the integrated circuit layout data, the integrated circuit
7 connection data, the set of one or more routing indicators and the one or
8 more locations to employ corner clipping, the routing path between the
9 first and second integrated circuit devices.

1 12. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes
3 identifying one or more obstacles that block the routing path,
4 determining one or more integrated circuit layout objects to be moved to provide
5 additional space for routing the routing path, and
6 determining, based upon the integrated circuit layout data, the integrated circuit
7 connection data, the set of one or more routing indicators and moving the
8 one or more integrated circuit layout objects, the routing path between the
9 first and second integrated circuit devices.

1 13. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes
3 examining data that indicates whether changes can be made to one or more layout
4 objects defined by the integrated circuit layout data to accommodate the
5 routing of the routing path, and
6 if the data indicates that changes can be made to the one or more layout objects
7 defined by the integrated circuit layout data to accommodate the routing of
8 the routing path, then
9 making one or more changes to the one or more layout objects defined by
10 the integrated circuit layout data, and
11 determining, based upon the integrated circuit layout data, the integrated
12 circuit connection data, the set of one or more routing indicators
13 and the one or more changes made to the one or more layout
14 objects, the routing path between the first and second integrated
15 circuit devices.

1 14. The method as recited in Claim 13, further comprising generating data that
2 specifies the one or more changes made to the one or more layout objects.

1 15. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes
3 determining a set of one or more routing targets to which the routing path is to be
4 routed, and
5 determining, based upon the integrated circuit layout data, the integrated circuit
6 connection data, the set of one or more routing indicators and the set of
7 one or more routing targets, the routing path between the first and second
8 integrated circuit devices.

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1 16. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes performing one or more
3 design rule checks on one or more portions of the routing path as the routing path
4 is being determined.

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1 17. The method as recited in Claim 16, further comprising performing a design rule
2 check on the updated integrated circuit layout data, wherein the design rule check
3 does not check one or more layout objects previously checked during
4 determination of the routing path.

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1 18. The method as recited in Claim 1, wherein determining the routing path between
2 the first and second integrated circuit devices includes
3 extending the routing path a specified amount to generate an extended portion of
4 the routing path, and
5 performing a design rule check on only the extended portion of the routing path.

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1 19. The method as recited in Claim 1, wherein all attachment and bend angles defined
2 by the updated integrated circuit layout data are multiples of ninety degrees.

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1 20. The method as recited in Claim 1, wherein one or more attachment or bend angles
2 defined by the updated integrated circuit layout data are multiples of other than
3 ninety degrees.

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1 21. A method for automatically verifying an integrated circuit layout, the method
2 comprising the computer-implemented steps of:

3 receiving integrated circuit layout data that defines a set of two or more layout
4 objects contained in the integrated circuit layout;
5 performing a first design rule check on a layout object from the set of two or more
6 layout objects by evaluating the layout object against specified design
7 criteria;
8 changing one or more values defined by the specified design criteria to generate
9 updated specified design criteria, wherein the changing of the one or more
10 values is performed after a specified amount of time has elapsed and is
11 made with respect to either the layout object or one or more other layout
12 objects from the set of two or more layout objects; and
13 performing a second design rule check on the layout object by evaluating the
14 layout object against the updated specified design criteria.

- 1 22. A method for automatically routing an integrated circuit, the method comprising
2 the computer-implemented steps of:
3 receiving integrated circuit layout data that defines a set of two or more integrated
4 circuit devices to be included in the integrated circuit;
5 receiving integrated circuit connection data that specifies one or more electrical
6 connections to be made between the integrated circuit devices;
7 determining, based upon the integrated circuit layout data and the integrated
8 circuit connection data, a set of two or more join points that are to be
9 electrically connected;
10 determining, based upon the integrated circuit layout data and the set of two or
11 more join points, one or more routing paths to connect the set of two or
12 more join points, wherein the one or more routing paths satisfy specified
13 design criteria; and

14 updating the integrated circuit layout data to generate updated integrated circuit
15 layout data that reflects the one or more routing paths.

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1 ~~23.~~ A method for automatically routing an integrated circuit, the method comprising
2 the computer-implemented steps of:
3 receiving integrated circuit layout data that defines a set of two or more integrated
4 circuit devices to be included in the integrated circuit;
5 receiving integrated circuit connection data that specifies one or more electrical
6 connections to be made between the integrated circuit devices;
7 determining, based upon the integrated circuit layout data and the integrated
8 circuit connection data, a routing path between first and second integrated
9 circuit devices that satisfies specified design criteria, wherein determining
10 the routing path between the first and second integrated circuit devices
11 includes
12 determining whether the distance to be routed for a portion of the routing
13 path exceeds a specified distance, and
14 if the distance to be routed for the portion of the routing path does not
15 exceed the specified distance, then routing the portion of the
16 routing path in a single step; and
17 updating the integrated circuit layout data to generate updated integrated circuit
18 layout data that reflects the routing path between the first and second
19 integrated circuit devices.

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1 24. A computer-readable medium carrying one or more sequences of one or more
2 instructions for automatically routing an integrated circuit, the one or more
3 sequences of one or more instructions including instructions which, when
4 executed by one or more processors, cause the one or more processors to perform

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1 26. The computer-readable medium as recited in Claim 24, wherein determining the
2 routing path between the first and second integrated circuit devices includes
3 identifying one or more obstacles that block the routing path,
4 determining, based upon the integrated circuit layout data, the integrated circuit
5 connection data and the one or more obstacles, one or more additional
6 routing indicators, and
7 determining, based upon the integrated circuit layout data, the integrated circuit
8 connection data, the set of one or more routing indicators and the one or
9 more additional routing indicators, the routing path between the first and
10 second integrated circuit devices.

1 27. The computer-readable medium as recited in Claim 24, wherein determining the
2 routing path between the first and second integrated circuit devices includes
3 identifying one or more obstacles that block the routing path,
4 changing specified straying limit criteria that defines a routing region in which the
5 routing path between the first and second integrated circuit devices may be
6 placed to generate changed specified straying limit criteria that defines a
7 modified routing region, and
8 determining, based upon the integrated circuit layout data, the integrated circuit
9 connection data, the set of one or more routing indicators and the changed
10 specified straying limit criteria, the routing path between the first and
11 second integrated circuit devices.

1 28. The computer-readable medium as recited in Claim 24, wherein determining the
2 routing path between the first and second integrated circuit devices includes
3 identifying one or more obstacles that block the routing path,

4 determining a set of one or more layer changes to allow the routing path to avoid
5 the one more obstacles, and
6 determining, based upon the integrated circuit layout data, the integrated circuit
7 connection data, the set of one or more routing indicators and the set of
8 one or more layer changes, the routing path between the first and second
9 integrated circuit devices.

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1 29. A system for automatically routing an integrated circuit, system comprising:
2 a data storage mechanism having stored therein
3 integrated circuit layout data that defines a set of two or more integrated
4 circuit devices to be included in the integrated circuit, and
5 integrated circuit connection data that specifies one or more electrical
6 connections to be made between the integrated circuit devices; and
7 a routing mechanism communicatively coupled to the data storage mechanism,
8 the routing mechanism being configured to
9 determine, based upon the integrated circuit layout data and the integrated
10 circuit connection data, a set of one or more routing indicators that
11 indicate a set of one or more preferable intermediate routing
12 locations for a routing path between first and second integrated
13 circuit devices from the set of two or more integrated circuit
14 devices,
15 determine, based upon the integrated circuit layout data, the integrated
16 circuit connection data and the set of one or more routing
17 indicators, the routing path between the first and second integrated
18 circuit devices, wherein the routing path satisfies specified design
19 criteria, and

20 update the integrated circuit layout data to generate updated integrated
21 circuit layout data that reflects the routing path between the first
22 and second integrated circuit devices.

1 30. The system as recited in Claim 29, wherein the routing mechanism is further
2 configured to determine the routing path by determining, based upon the
3 integrated circuit layout data, the integrated circuit connection data, bias direction
4 criteria and straying limit criteria, the routing path between the first and second
5 integrated circuit devices, wherein the bias direction criteria specifies a preferred
6 routing direction for a routing path between first and second integrated circuit
7 devices from the set of two or more integrated circuit devices and the straying
8 limit criteria defines a routing region in which the routing path between the first
9 and second integrated circuit devices may be placed.

1 31. The system as recited in Claim 29, wherein the routing mechanism is further
2 configured to determine the routing path between the first and second integrated
3 circuit devices by
4 identifying one or more obstacles that block the routing path,
5 determining, based upon the integrated circuit layout data, the integrated circuit
6 connection data and the one or more obstacles, one or more additional
7 routing indicators, and
8 determining, based upon the integrated circuit layout data, the integrated circuit
9 connection data, the set of one or more routing indicators and the one or
10 more additional routing indicators, the routing path between the first and
11 second integrated circuit devices.

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1 32. The system as recited in Claim 29, wherein the routing mechanism is further
2 configured to determine the routing path between the first and second integrated
3 circuit devices by
4 identifying one or more obstacles that block the routing path,
5 changing specified straying limit criteria that defines a routing region in which the
6 routing path between the first and second integrated circuit devices may be
7 placed to generate changed specified straying limit criteria that defines a
8 modified routing region, and
9 determining, based upon the integrated circuit layout data, the integrated circuit
10 connection data, the set of one or more routing indicators and the changed
11 specified straying limit criteria, the routing path between the first and
12 second integrated circuit devices.

1 33. The system as recited in Claim 29, wherein routing mechanism is further
2 configured to determine the routing path between the first and second integrated
3 circuit devices by
4 identifying one or more obstacles that block the routing path,
5 determining a set of one or more layer changes to allow the routing path to avoid
6 the one more obstacles, and
7 determining, based upon the integrated circuit layout data, the integrated circuit
8 connection data, the set of one or more routing indicators and the set of
9 one or more layer changes, the routing path between the first and second
10 integrated circuit devices.

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